

ABSTRACT OF THE DISCLOSURE

An inexpensive and simple circuit for improving an image quality of a dynamic image, with appropriate processing flexibly performed for dynamic image qualities also for a plurality of input signal sources. An image processing device comprises a memory unit having a memory region for storing images of at least one screens, a memory control unit for performing an input system operation to write image data to the memory unit on the basis of a first clock and a first image synchronizing signal and for performing an output system operation to output image data read out from the memory unit on the basis of a second clock and a second image synchronizing signal, a clock generating unit for generating the second clock, and a synchronizing control unit for inputting the second clock and for outputting the second image synchronizing signal, wherein the synchronizing control unit generates a third image synchronizing signal asynchronous to the first image synchronizing signal by dividing the second clock and a fourth image synchronizing signal with being synchronized to the first image synchronizing signal by using the second clock and selects one of the third image synchronizing signal and the fourth image synchronizing signal to output it as the second image synchronizing signal. There can be a plurality of the input image signals; if so, the synchronizing control

[illegible]